



## CURRICULUM GUIDE: OFFICIAL COURSE OUTLINE

Course Code	CPSC 150	Course Title	Computer Organization and Logic Design			
Credit Value	3	Department	Mathematics and Science			
No. of weeks	14	Hrs. per week	<i>Lecture</i>	<i>Tutorial</i>	<i>Laboratory</i>	<i>Total</i>
			3	0	0	3
Course Description	This course introduces the internal operation of computer systems. Topics include: Boolean algebra and combinational and sequential logic design, basic computer system organization, processor, memory, and input/output, information representation: number systems, integer floating point representation and character encoding, and an introduction to assembly language programming.					
Prerequisite(s)	ENGL 098 and CPSC 111 ( <i>CPSC 115/ MATH 115 is strongly recommended</i> )					
Initial Articulation Targets	<i>UBC</i>	<i>SFU</i>	<i>UVic</i>	<i>UNBC</i>	<i>TRU</i>	
	CPSC 1 <sup>ST</sup> (3)	CMPT 150 (3)	CSC 1XX (1.5)	CPSC 230 (4)	COMP 2130 (3)	
	For updated information on the transferability of this course, please consult the BC Transfer Guide, <a href="http://www.bctransferguide.ca">www.bctransferguide.ca</a>					
Learning Outcomes	<p>Upon successful completion of this course, the student will be able to:</p> <ul style="list-style-type: none"> <li>Describe how computers represent and manipulate various data types</li> <li>Demonstrate an understanding of computer arithmetic and convert between different number systems</li> <li>Demonstrate how fundamental high-level programming constructs are implemented at the machine-language level</li> <li>Write an assembly language program that can input, process, and output results</li> <li>Demonstrate an understanding of basic computer architecture</li> <li>Use Boolean algebra as related to designing computer logic, through simple combinational and sequential logic circuits</li> <li>Describe how a CPU performs instructions during the fetch-decode-execute cycle and how the memory supports its actions.</li> </ul>					
Content	<p><b>Core</b> topics – all of the following will be covered:</p> <ul style="list-style-type: none"> <li>Introduction to basic CPU architecture, addressing modes and program execution</li> <li>Data representation: number systems, integer floating point representation, character encoding</li> <li>Introduction to Boolean algebra</li> <li>Combinational logic design and applications: adders, encoders, decoders, multiplexers, etc.</li> <li>Sequential logic design and applications: flip-flops, registers, and finite state machines</li> <li>Control: data path construction, buses and microprogrammed control</li> </ul>					



	<ul style="list-style-type: none"> <li>• Memory devices: read-only memory (ROM), random access memory (RAM)</li> <li>• Introduction to Assembly language programming: branching, iteration, and subroutines.</li> </ul> <p>Additional topics may also be covered, at the discretion of the instructor.</p>		
Methods of Instruction	Lectures, assignments, computer laboratory work, projects, assigned reading, quizzes, examinations		
Required Textbook(s)	<p>The following textbook(s) is/are required, or approved equivalent(s).</p> <p>Morris Mano, M. and Charles R. Kime. Logic &amp; Computer Design Fundamentals. 5th Ed., Pearson Publishing, 2016. ISBN-13: 9780133760637</p> <p>Supplemental Text:</p> <p>Capilano Computing Systems. Logicworks 5: Interactive Circuit Design Software. Prentice Hall, 2003. ISBN-13: 9780131456563, Manual &amp; software for digital hardware simulation for installation on laptops with MS Windows</p>		
Required Equipment and Technology	<p>Students are required to have a computer with internet access.</p> <p>The following resources are provided by the College:</p> <ul style="list-style-type: none"> <li>• Office 365</li> <li>• Student email</li> </ul>		
Homework Hours	At minimum, students can expect one hour of homework for every hour of instructional time.		
Evaluation	<i>Component</i>	<i>% Value</i>	
	Assignments and quizzes	10-20%	
	Labs and projects	10-20%	
	Midterm examinations (1-2)	20-40%	
	Final examination	30-35%	
Completion Requirements	The minimum grade to pass this course is D (50%). Unless otherwise stated, a minimum grade of C- (55%) is required for this course to fulfil a prerequisite.		
Course Designer(s)	Ahmed Malki, Ph.D., Department of Computer Science, Alexander College	Consultant(s), if applicable	John Edgar, M.Sc., School of Computing Science, Simon Fraser University
Dean's Approval	Barbara Moon, Ph.D., Dean of Arts and Sciences, Alexander College	Dean's Approval Date	October 16, 2019
Curriculum Committee Approval Date	October 16, 2019	First Term Offered	Fall 2020
Last Review Date	July 29, 2024	Next Review Date	July 29, 2029



# Alexander College

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Revision History

July 29, 2024 - Added 1 lab hour and revised evaluation ranges by Kelly Cheung.

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